

MICHAEL DEEDS

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SUMMARY

Electrical Engineer with 18 years of experience leading the design of a variety of electronics products. Strengths include engineering project management, product definition and architecture, embedded systems hardware and software design, FPGA and ASIC design.

PROFESSIONAL EXPERIENCE

Cardinal Peak, LLC Louisville, CO

Jan 2009 to Present

Partner, Project Manager

Managed the design of a digital video recording system for use in police cars. Worked with the client to define the product requirements, budget, schedule, and staffing. Defined the system architecture and high level design. Managed a 7 – 9 person team to complete the project on time and within the proposed budget.

MobilePeak Systems Boulder, CO – Shanghai, China

Oct 2005 to Oct 2008

Member of Technical Staff

Designed portions of a 3G wireless user terminal, with custom System-On-Chip (SoC).
Prototyped SoC subsystems in an FPGA.
Designed and debugged layer 1 WCDMA/HSDPA software (3GPP 34.121)
Mentored and collaborated with a large team of colleagues in Shanghai, China.

Independent Consultant/Subcontractor

May 2004 to Dec 2005

Fidelity Comtech – Designed a 533MHz single board computer for use in a wireless access point.
CEPD/ValleyLab – Created VHDL and Verilog testbenches to verify surgical instruments.
CEPD/Ascend Geophysical – Tested and debugged a seismic data collection system. Assisted with a system field trial in Belize.

Precision Photonics Corp Boulder, CO

Aug 2001 to April 2004

Chief Engineer

Responsible for all electronics and software design in company.
Managed project schedules and hiring of consultants.
Designed optical Test & Measurement products including swept wavelength meters and tunable laser spectrum analyzers.

Independent Consultant/Subcontractor

May 2000 to Aug 2001

Precision Photonics Corp – Developed a tunable laser swept wavelength meter.
CEPD/DataPlay, Inc. – Created a VHDL/Verilog testbench to verify an optical disk controller ASIC.

**Lotus Energy
Kathmandu, Nepal**

Nov 1999 to Jan 2000

Volunteer

Designed a smart battery charger for an electric vehicle.

**QUALCOMM, Inc.
Boulder, CO**

July 1995 to July 1999

Senior Hardware Engineer

Lead the digital design of a CDMA cellular base station. Worked to transition the design into a high volume manufacturable product.

Lead the electrical design, and designed the primary controller for a CDMA wireless PBX.

**Micro Motion Inc.
Boulder, CO**

Feb 1993 to Feb 1995

Research Engineering Intern

Developed hardware and firmware for a DSP based mass-flowmeter. Analyzed cost and performance trade-offs between existing system and DSP based system.

**Otsuka Electronics
Fort Collins, CO**

March 1992 to Dec 1992

Contract Engineer

Designed digital interfaces between Magnetic Resonance Imaging (MRI) subsystems.

■ **EDUCATION**

University of Colorado, Boulder

MSEE, December 1996. Focus: Computer Architecture

Marquette University

BSEE, December 1991